

RTL8411/8401P/8401 Circuit

Rev. 1.60

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2.	POWER
3.	CARD READER
4.	QFN 64 CONTROLLER
5.	QFN 48 COLAY

Change Notice from 1.00 to 1.10

1. Add Note.

Change Notice from 1.10 to 1.20

1. Remove Large Capacity.

Change Notice from 1.20 to 1.30

1. Modify Power Source.
2. Modify Combo Socket.

Change Notice from 1.30 to 1.40

1. ADD RTL8411 Schematic

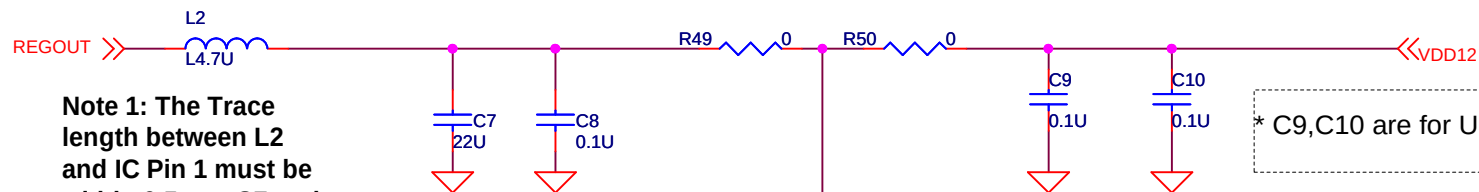
Change Notice from 1.40 to 1.50

1. ADD Resister to GND For Card Reader 3.3V.

Change Notice from 1.50 to 1.60

1. ADD Note for RTL8401.

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Title BAROSSA QFN 48/64 QA Board : INDEX		
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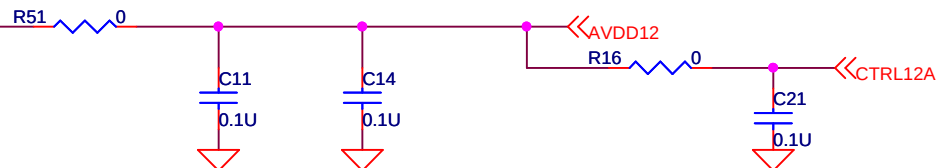


Note 1: The Trace length between L2 and IC Pin 1 must be within 0.5 cm. C7 and C8 to L2 must be within 0.5cm. Refer to Layout guide for more detail.

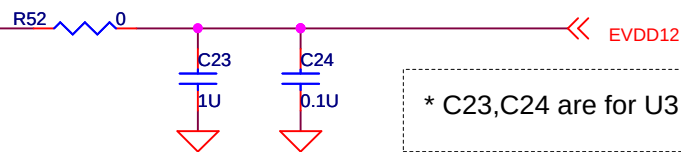
* C9,C10 are for U3 VDD12 pins--15 and 48.

****When Using RTL8401****

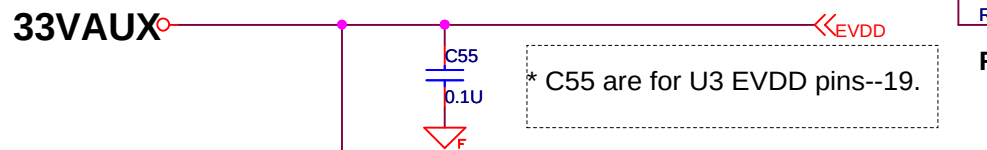
Remove L2,C7,C8,C9,C10,C11,C14,R16,R49,R50,R51,R52,R53
Keep C21,C23,C24,C27 (Close to RTL8401)



* C11,C14 are for U3 AVDD12 pins--28 and 42. Keep C21 and Remove R16 For RTL8401



* C23,C24 are for U3 EVDD12 pins--24



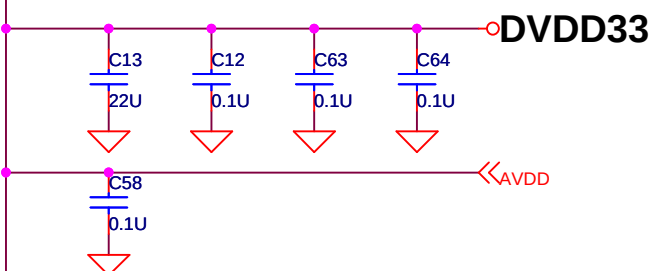
* C55 are for U3 EVDD pins--19.



Remove R53 For RTL8401

****When Using RTL8411/8401P****

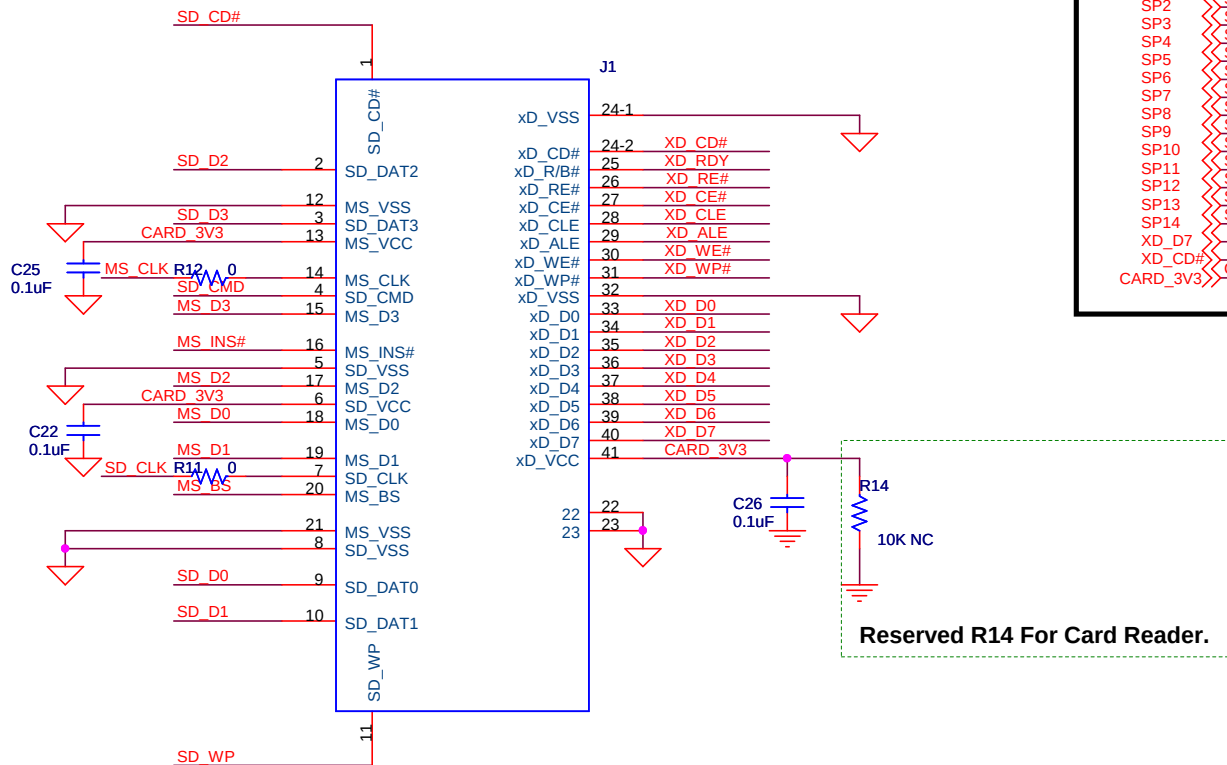
	C21	C27	R16	R49	R50	R51	R52	R53
Enable Switch Regulator	X	O	O	O	O	O	O	X
Disable Switch Regulator	O	X	X	X	O	X	X	O



* C56,C57 are for U3 D3V3 U3 pins--60.

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Flash Card Combo Socket



SP1	>> SP1	XD RDY	SD WP	MS CLK
SP2	>> SP2	XD RE#		MS INS#
SP3	>> SP3	XD CE#	SD D1	
SP4	>> SP4	XD CLE	SD D0	MS D7
SP5	>> SP5	XD ALE	SD D7	MS D3
SP6	>> SP6	XD WE#	SD CD#	
SP7	>> SP7	XD WP#	SD D6	MS D6
SP8	>> SP8	XD D0	SD CLK	MS D2
SP9	>> SP9	XD D1	SD D5	MS D0
SP10	>> SP10	XD D2	SD CMD	
SP11	>> SP11	XD D3	SD D4	MS D4
SP12	>> SP12	XD D4	SD D3	MS D1
SP13	>> SP13	XD D5	SD D2	MS D5
SP14	>> SP14	XD D6		MS BS
XD D7	>> XD D7			
XD_CD#	>> XD_CD#			
CARD_3V3	>> CARD_3V3			

Share Pin

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